

Chaemin Lim

High Performance Computing Platform (HPCP) Lab.
College of Computing
Yonsei University, Seoul, Korea (Republic of)

cmlim@yonsei.ac.kr
github.com/janoslim

RESEARCH INTERESTS

- Database/Operating Systems on Modern Hardwares
- Hardware/Software Co-optimization
- Concurrent Computing
- Heterogeneous Computing

PUBLICATIONS

Chaemin Lim, Suhyun Lee, Jinwoo Choi, Jounghoo Lee, Seongyeon Park, Hanjun Kim, Jinho Lee, and Youngsok Kim, "Design and Analysis of a Processing-in-DIMM Join Algorithm: A Case Study with UPMEM DIMMs", SIGMOD 2023
Jinwoo Choi¹, Jaeyeon Kim¹, **Chaemin Lim**¹, Suhyun Lee, Jinho Lee, Dokyung Song, and Youngsok Kim, "GuardiaNN: Fast and Secure On-Device Inference in TrustZone Using Embedded SRAM and Cryptographic Hardware", Middleware 2022

PROJECTS

- **Development of New Concept PIM Semiconductor Leading Technology**
Institute for Information and communication Technology Planning and evaluation (IITP) *2022-Present*
- **STACK: Smart, Attack-Resistant Internet of Things Networks**
Korea Institute for Advancement of Technology (KIAT) *2021-Present*

EDUCATION

- **Yonsei University** Seoul, Korea
Master/Ph.D Student, Artificial Inteligence *March 2021 - Present*
- **Hankuk University of Foreign Studies** Seoul, Korea
B.Eng. in Computer Engineering *March 2017 - February 2021*
- **Hankuk University of Foreign Studies** Seoul, Korea
B.A. in Journalism *March 2017 - February 2021*

TEACHING EXPERIENCE

- **Logic Circuit Design (CSI2111)**: Teaching Assistant, Fall 2022
- **Open Source Projects (CSI2115)**: Teaching Assistant, Fall 2021
- **Information Security (CSI4109)**: Teaching Assistant, Spring 2021

SKILLS

- **C, C++, Python, Verilog**
- **Korean** (Native), **English** (Fluent)